

The Abstract has been amended as follows:

ABSTRACT OF THE DISCLOSURE

An electronic control device with a parallel databus and a plurality of assemblies connected to the databus. The assemblies each include a processor and
5 a memory device and are connected to the databus with a bus controller. The data
are transmitted between a transmitter assembly and a receiver assembly with
messages. The data bus essentially corresponds to the MULTIBUS II. The bus
controller of the transmitter assembly is fashioned such that, without making use
of the processor of the transmitter assembly, it reads data stored in the memory
10 device of the transmitter assembly in response to a request message of the receiver
assembly and sends them to the receiver assembly.

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